Docket Template

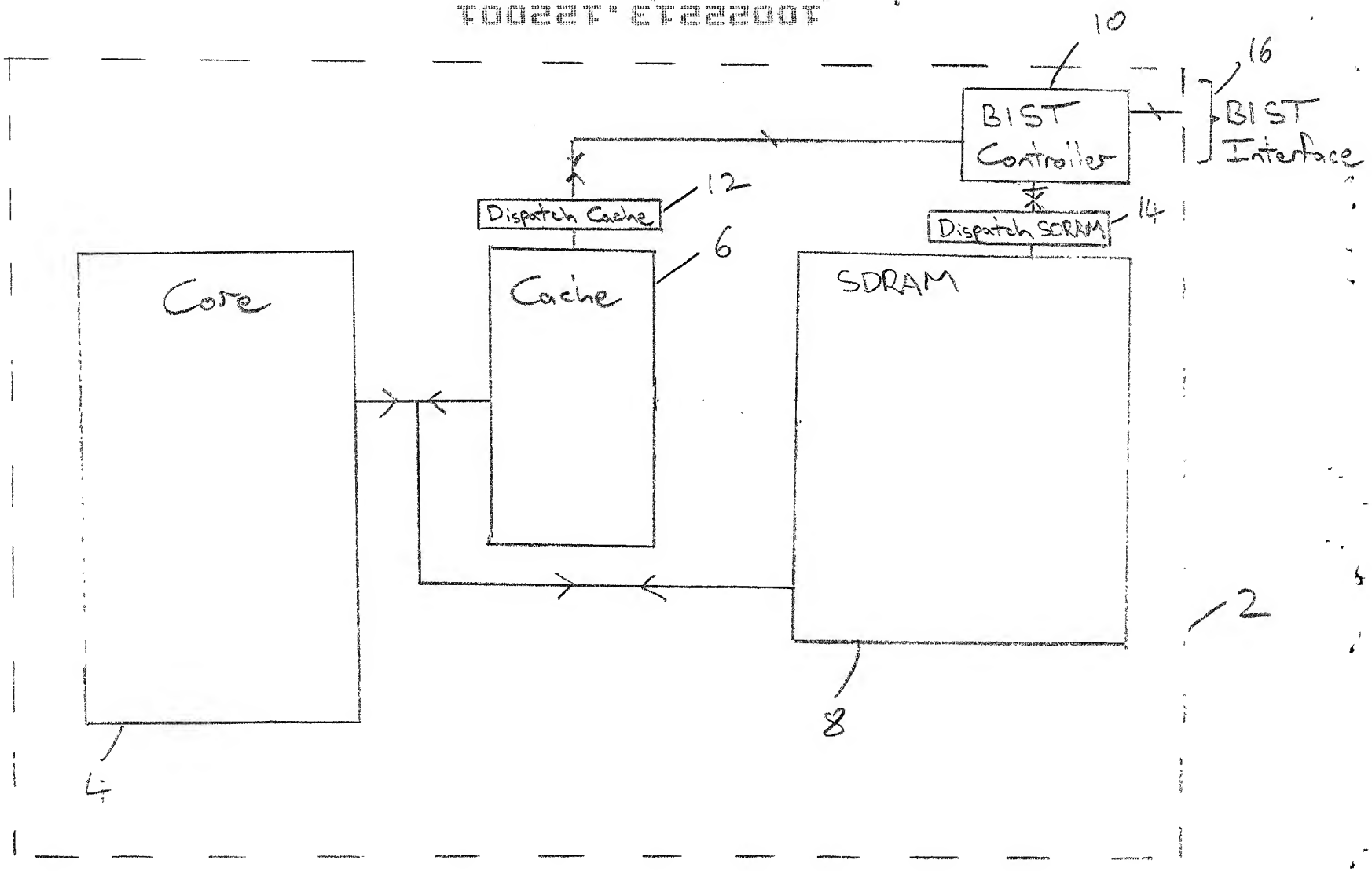

Fig. 1

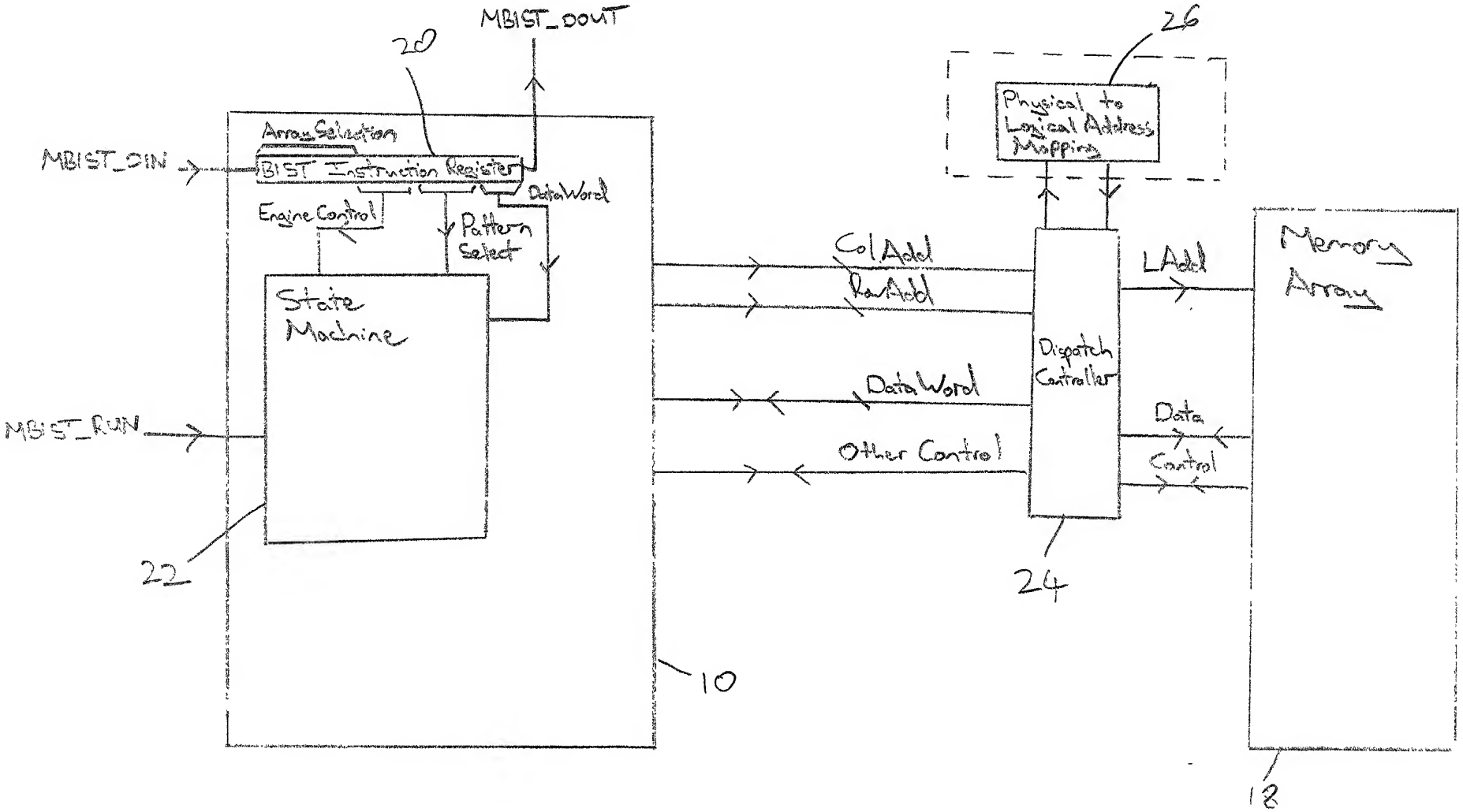
MBIST_DOUT
20
26
Physical to Logical Address Mapping
Array Selection
MBIST_DIN
BIST Instruction Register
DataWord
Engine Control
Pattern Select
State Machine
ColAdd
RowAdd
LAdd
Memory Array
Dispatch Controller
DataWord
Data
Control
MBIST_RUN
Other Control
22
24
10
18

Fig. 2

Document Example

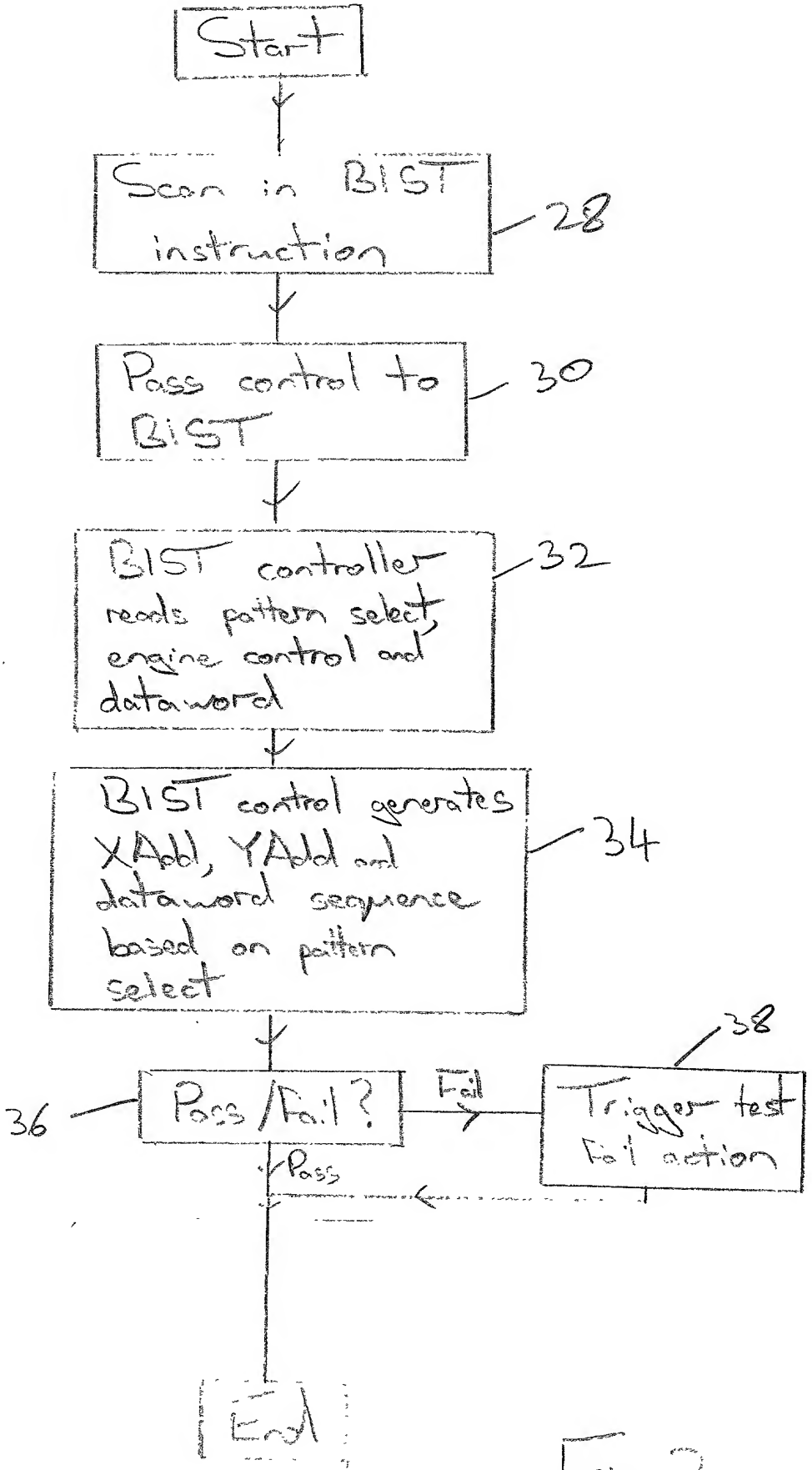

Fig. 3

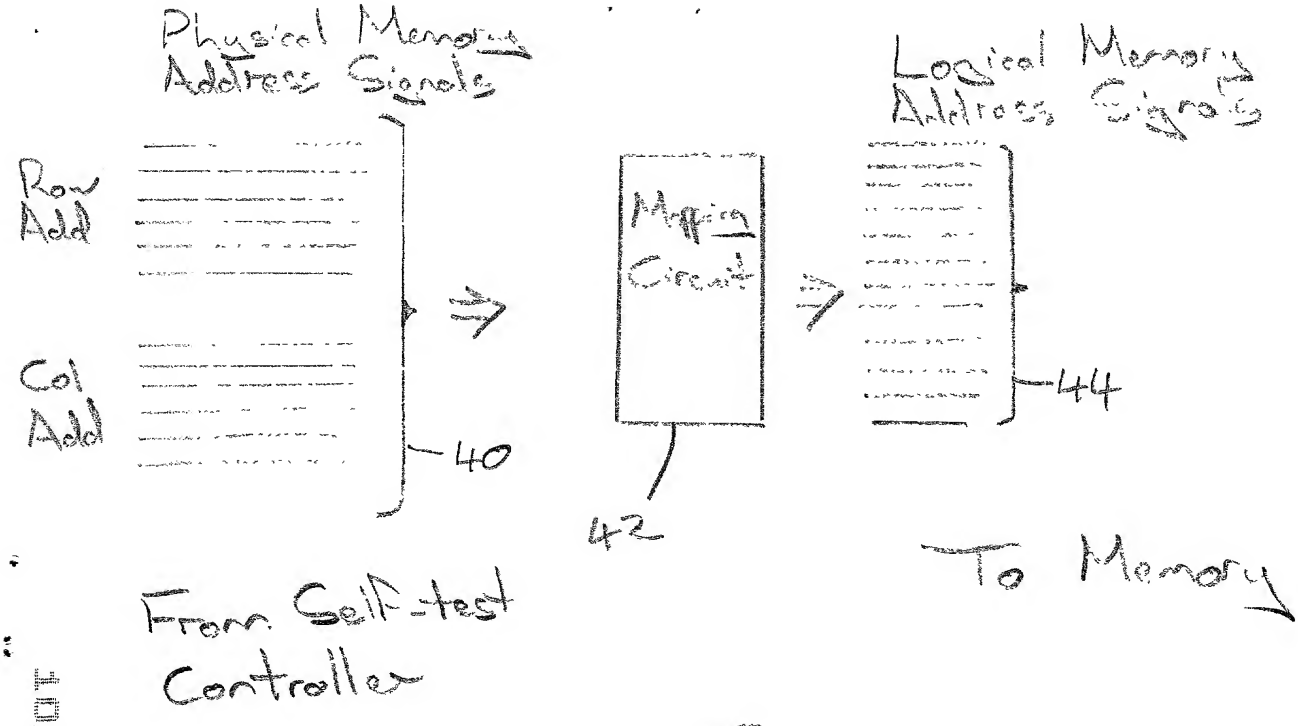

Fig. 4

Document Produced Natively

LA [8:0] when implemented has 32 rows, 8 cols, 1 block select

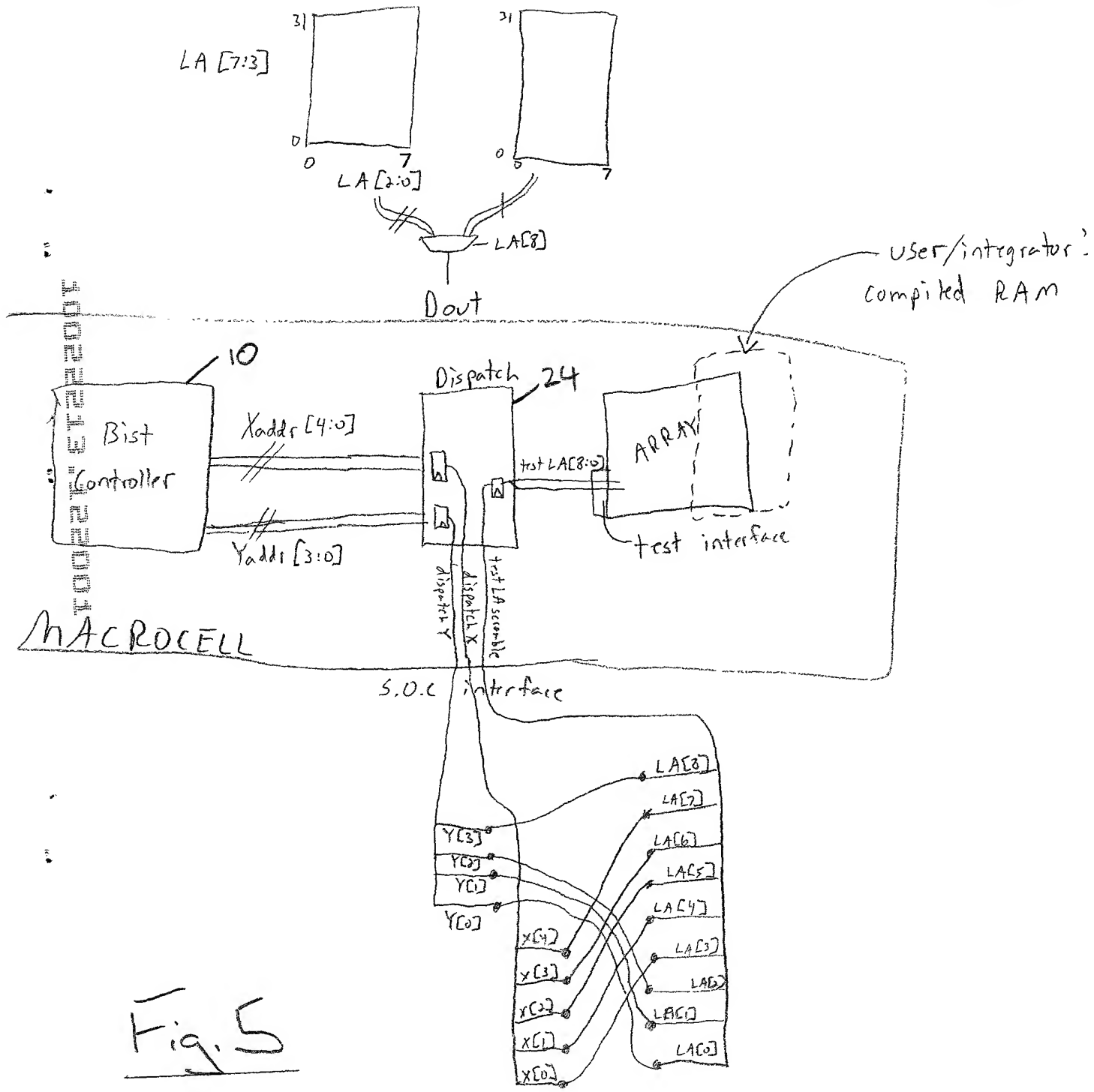

Fig. 5